

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/531,287 Confirmation No. 4868  
Applicant : T. TANIMOTO et al  
Filed : April 14, 2005  
Title : COMPILER AND LOGIC CIRCUIT DESIGN METHOD  
TC/AU : 2825  
Examiner : S. Parihar  
Docket No. : TAM-103  
Customer No.: 24956

Commissioner for Patents  
Mail Stop Amendment  
P.O. Box 1450  
Alexandria, VA 22313-1450

March 21, 2007

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**AMENDMENT**

Sir:

In response to the Office Action of September 21, 2006, please amend the above-identified application as follows.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks / Arguments** begin on page 5 of this paper.

**A Petition and fee for a three-month Extension of Time also accompanies this response.**